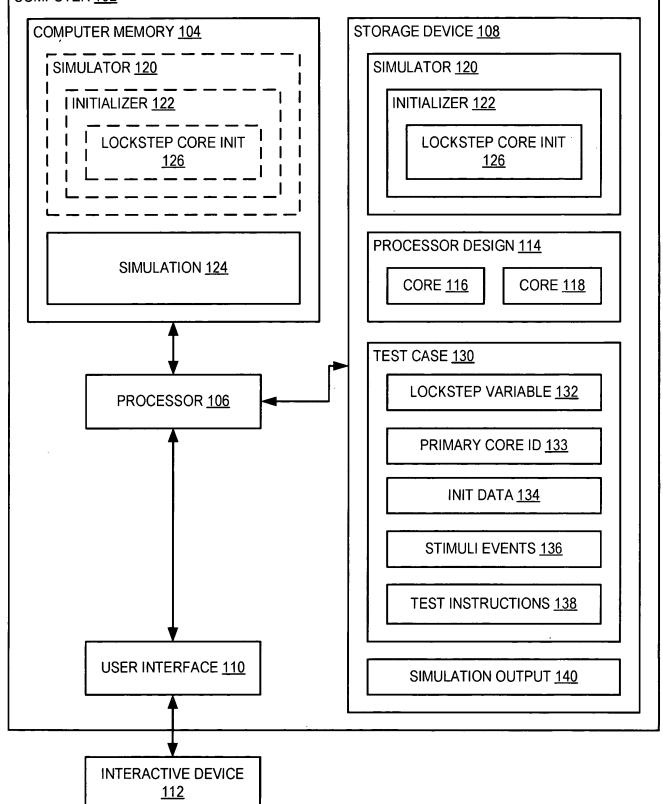
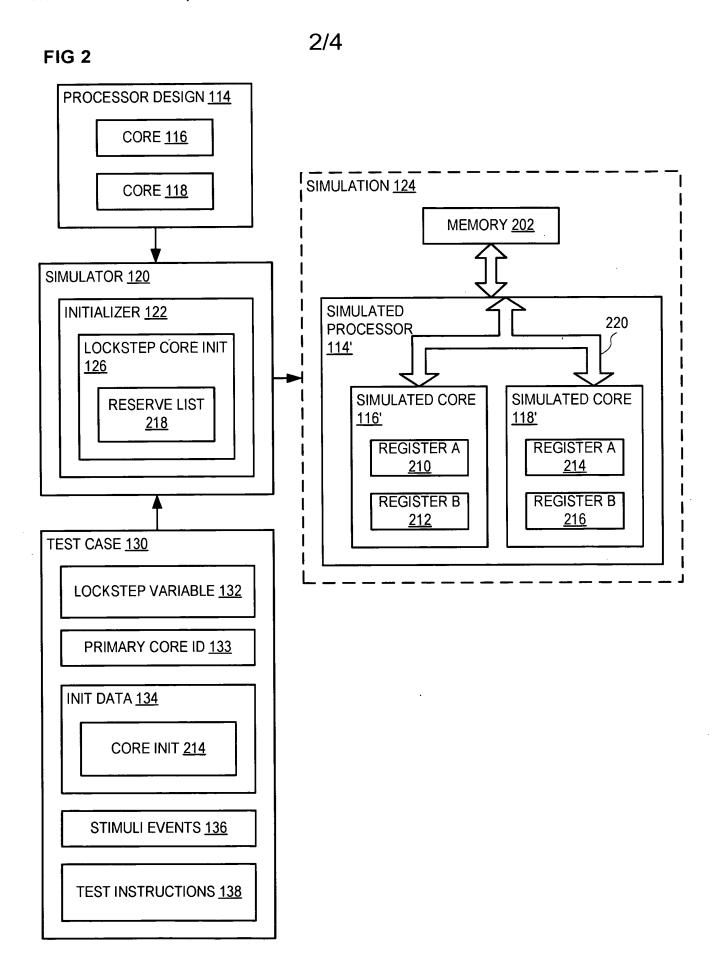
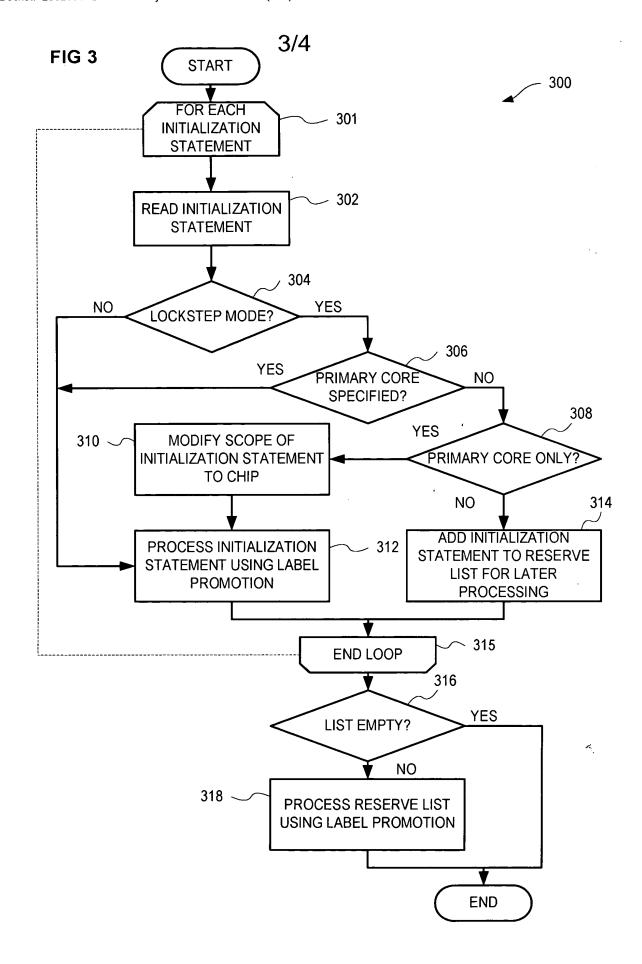
1/4 100 FIG₁ **COMPUTER 102 COMPUTER MEMORY 104** STORAGE DEVICE 108 SIMULATOR 120 | SIMULATOR 120 I INITIALIZER 122 **INITIALIZER** 122 LOCKSTEP CORE INIT LOCKSTEP CORE INIT 126 <u>126</u> PROCESSOR DESIGN 114 SIMULATION 124 CORE <u>116</u> **CORE** <u>118</u> TEST CASE 130 **LOCKSTEP VARIABLE 132** PROCESSOR 106 PRIMARY CORE ID 133 INIT DATA 134







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